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**Bib Data Sheet** 

**CONFIRMATION NO. 5457** 

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APPLICANTS										
Takashi Yamada, Kanagawa-ken, JAPAN; Hajime Nagano, Kanagawa-ken, JAPAN; Ichiro Mizushima, Kanagawa-ken, JAPAN; Tsutomu Sato, Kanagawa-ken, JAPAN; Hisato Oyamatsu, Kanagawa-ken, JAPAN; Shinichi Nitta, Kanagawa-ken, JAPAN;										
** CONTINUING DATA **********************************										
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** FOREIGN APPLICATIONS ************************************										
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Foreign Priority claimed yes no STATE OR SHEETS TOTAL INDEPE								INDEDENDENT		
35 USC 119 (a-q) conditions (A) L. / L.   AAAAAAA					DRA	SHEETS TOTAL CLAIM 19 20		MS	INDEPENDENT CLAIMS 2	
ADDRESS 22850										
TITLE										
Semiconductor chip which combines bulk and SOI regions and separates same with plural isolation regions										
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